

10037039
U.S. PTO

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10037039	FILING DATE 12/21/2001	CLASS 341	SUBCLASS 155	GAU 2813 3600	EXAMINER L. Mai
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**APPLICANTS: Breinlinger Richard;

**CONTINUING DATA VERIFIED: NO *LM*

** FOREIGN APPLICATIONS VERIFIED: NO *LM*

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <i>LM</i>	<input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes	<input type="checkbox"/> no	SAA-83
Verified and Acknowledged Examiners's initials	<i>LM</i>		

TITLE : Low cost multi-range input circuit for industrial analog input modules *LM*
U.S. DEPT. OF COMM./PAT & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due Date Paid

TERMINAL

DISCLAIMER

Assistant Examiner

Primary Examiner

PREPARED FOR ISSUE

CLAIMS ALLOWED

Total Claims	Print Claim for O.G.
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DRAWING

Sheets Draw.	Figs. Draw.	Print Fig.
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Application Examiner

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